

# Fault Coverage Reports

## Introduction

Selecting Reports on the onTAP Menu bar presents a list of testability report files in the project folder currently open on the Development screen. Each time an SVF test file is compiled on the Development screen, a corresponding TestabilitySurvey file is created and those are the files shown in the list. The following options are available and can be selected from a pop-up menu bar after selecting a file.

The screenshot shows a window titled "Project reports and files" with a menu bar containing "Sum Checked", "Testability", "Diagnose Mbg", "Test Mbg", and "Help!". Below the menu bar is a table of report files with the following columns: Report Files, Jtag Opens, Jtag Shorts, All Opens, All Shorts, JTAG Stuck Pins, and All Stuck Pins. The table lists various report files with their respective coverage percentages. Below the table is a section for "Project BSDL Files...select to view" listing several .bsd files. At the bottom is a section for "Project Netlist".

Report Files	Jtag Opens	Jtag Shorts	All Opens	All Shorts	JTAG Stuck Pins	All Stuck Pins
<input type="checkbox"/> SummaryReport						
<input type="checkbox"/> clock_test	0.0%	0.0%	0.0%	0.0%		
<input type="checkbox"/> DDR2_16_bit	0.0%	0.0%	0.0%	0.0%		
<input checked="" type="checkbox"/> DDR2_16_bit_v3	20.6%	0%	3.7%	0%	11.3%	28.7%
<input type="checkbox"/> DDR2_16_bit_v3_exp	0.0%	0.0%	0.0%	0.0%		
<input checked="" type="checkbox"/> DDR2_64_bit	37.5%	0%	6.8%	0%	23.8%	65.6%
<input type="checkbox"/> extended_shorts	45.3%	93.3%	24.9%	60.1%		
<input type="checkbox"/> interconnect hank	43.9%	90.0%	9.6%	47.6%		
<input type="checkbox"/> interconnect	43.7%	90.0%	9.6%	47.6%		
<input type="checkbox"/> interconnect2	42.7%	89.7%	9.0%	47.3%		
<input type="checkbox"/> interconnect3	43.9%	89.9%	9.6%	47.5%		
<input checked="" type="checkbox"/> interconnect4	57.9%	91.0%	10.5%	50.8%	76.3%	57.9%
<input type="checkbox"/> interconnect5	44.9%	90.0%	10.0%	47.6%		
<input type="checkbox"/> interconnect_guards	42.1%	69.7%	9.1%	36.8%		
<input type="checkbox"/> interconnect_refer...	43.9%	90.2%	9.6%	47.7%		

Project BSDL Files...select to view

- xc5vh50\_ff676.bsd
- xc95144xl\_tq100.bsd
- xccace\_tq144.bsd
- xcf32p\_vo48.bsd

Project Netlist

1. Update and summarize reports for all of the files which have been checked. The results are placed in a summary file named TestabilitySurvey.txt. Summary fault scores for each checked file as well as for the summary report are shown when this is done.
2. View the summarized test report.
3. View the testability and fault coverage report, TestabilitySurvey\_<test\_name>.txt.
4. View the test template and test results (.test) file for the selected filename. The results from the last test run are shown.
5. View any diagnostic messages from last test run for the selected file.
6. Access this Help file

<input checked="" type="checkbox"/>	DDR2_16_bit_v3	20.6%	0%	3.7%
<input type="checkbox"/>	DDR2_16_bit_v3_exp	0.0%	0.0%	0.0%
<input checked="" type="checkbox"/>	DDR2_64_bit	37.5%	0%	6.8%
Update and summarize reports for checked files. View summary report for all files. View this file's testability and fault coverage. View this file's test template and test results (.test file). View this file's test failures and diagnostic messages (.fail file). Access Help				
<input type="checkbox"/>	interconnect_refer...	43.9%	90.2%	9.6%

## BSDL FILES

BSDSL files used in the project are shown and may be viewed by clicking on them.