Development

The onTAP Development System includes all of the necessary software tools you need to develop and run comprehensive and reliable onTAP tests, delivering robust JTAG solutions. With over 2 dozen CAD netlist readers, a built-in netlist merge tool, and ProScan - the graphical debug environment - the onTAP Development system will speed up your project development time, and keep costs under control. These tools enable the ability to quickly and easily develop, run, and debug tests ranging from single chain applications, to multiple chain applications with multi-die modules, merged sub-assemblies, and multi-drop configurations. onTAP can accommodate unlimited circuit size.

Automatic Test Generation Delivers More Comprehensive Fault Coverage

Based on Flynn Systems years as an industry leader in the Automatic Test Generation market, onTAP is incorporates aspects of ATG to speed test development, and deliver higher, more accurate test fault coverage with its boundary scan tests. Designed to require minimal user input, onTAP references the application netlist(s), collects critical test data during development, and develops a “test-to-print” solution. Any changes made to the application, must be subsequently updated and reflected in the netlist in order for onTAP to perform the most comprehensive test possible.

Highlights: Development

- Complete system
- Highly automated test development
- CAD Netlist Based - Over 2 Dozen CAD Netlist Readers Available
- Familiar notebook style
- Automatic chain detection and verification
- Netlist browser
- Netlist merge
- Flexible, reusable, reliable, automatic tests
- Flexible, reusable Flash and memory models
- Development Project Assistant Guides You Through Test Development
- ProScan graphical debug enabled
- Circuit browser
- Flash format translators

Intuitive & Organized Test Development

You’ll notice the intuitive and organized note book style tabs, each labeled according to the respective part of the test, collecting critical inputs and marking your progress through test development. Detailed instructions eliminate guess work when inserting the necessary information needed to develop the most comprehensive tests for your project.

Automatic JTAG Chain Detection & Verification

Identify the JTAG chain and verify the order with this feature. onTAP references the netlist(s) and interrogates the chain and reports back via the attached USB cable to determine the correct device order.

Manual overrides are available with a drag-and-drop tool, allowing users to re-order devices to their specifications.

Flexible, Reusable & Reliable

onTAP is designed for flexibility. That is why our models are created using the familiar C-program like DTS language, and are not project specific. All models can be saved for use from one project to the next, and can easily be altered to meet the needs of a new project or a new device type in a family.

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